

FIG. 1

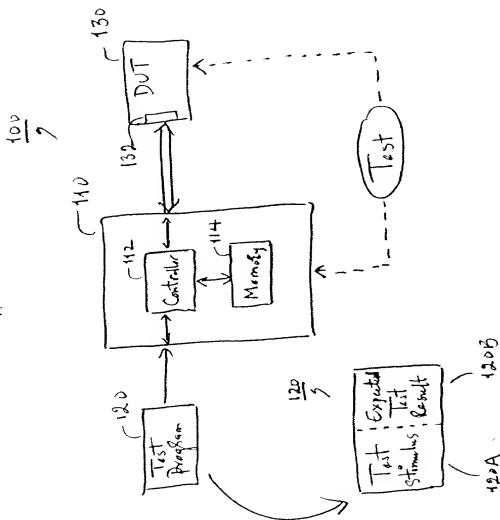


FIG. 2

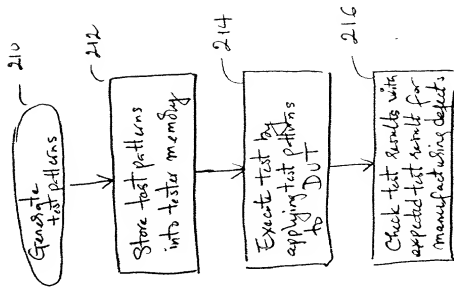


Fig. 3. RIT Tool Flow

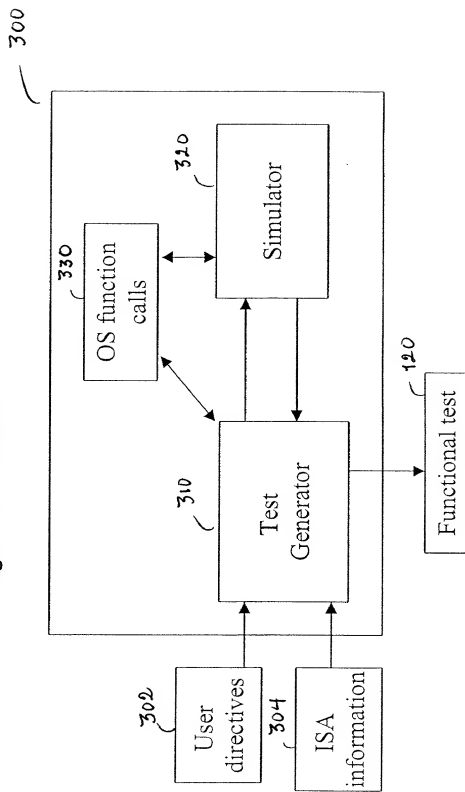


FIG. 4

400

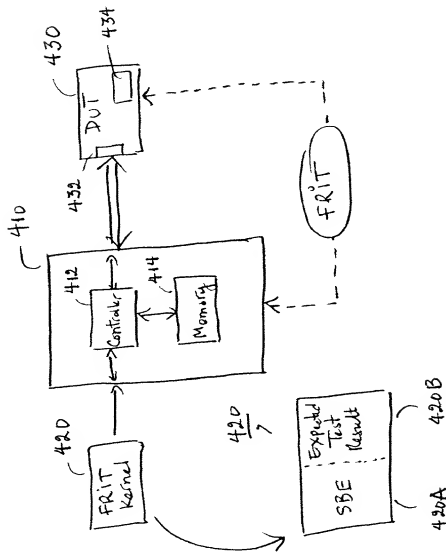


FIG. 5

420A

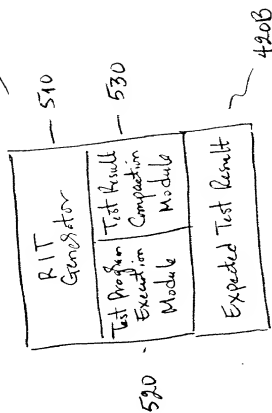


Fig 6. Concept of SBE Generation Tool

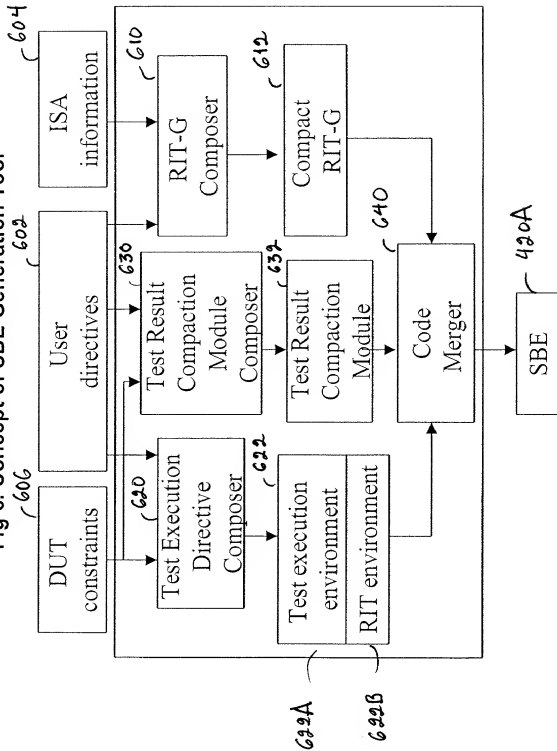
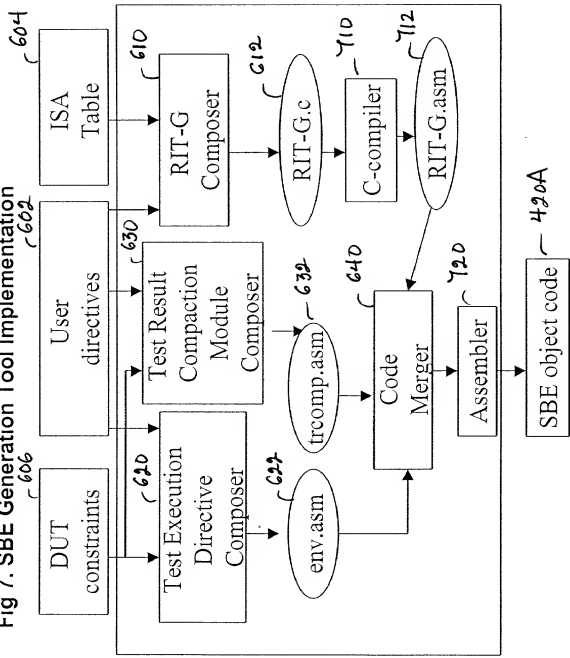


Fig 7. SBE Generation Tool Implementation



600

**Fig 8: Instruction generation algorithm**

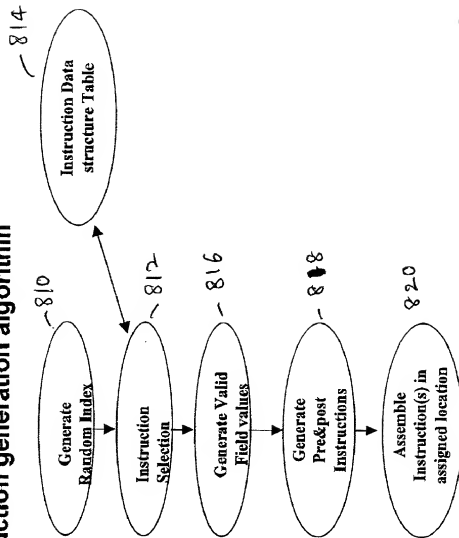


FIG. 9

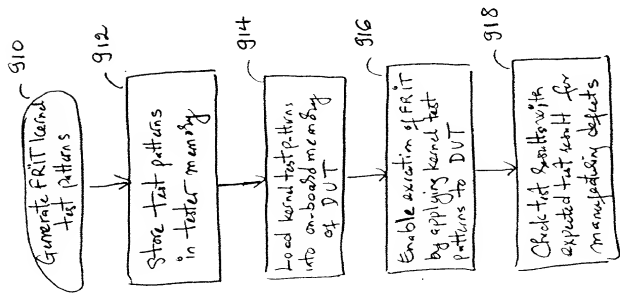


FIG. 10

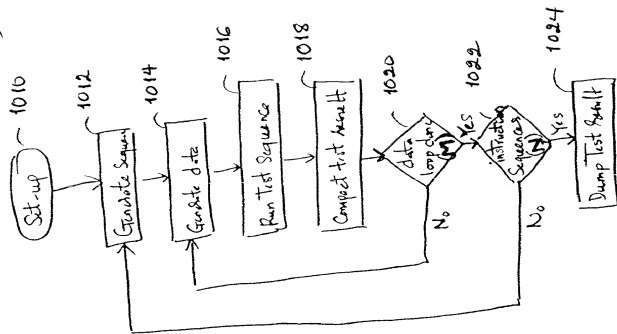
916  
5

FIG. 11

## FRIT Kernel (FRIT-K) Execution Sequence

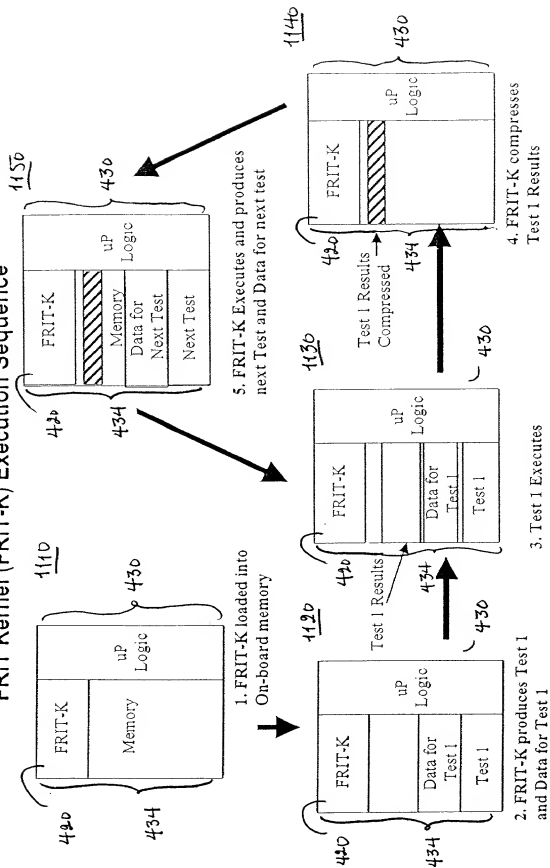


FIG. 12 DUT Memory Image: Beginning of Test

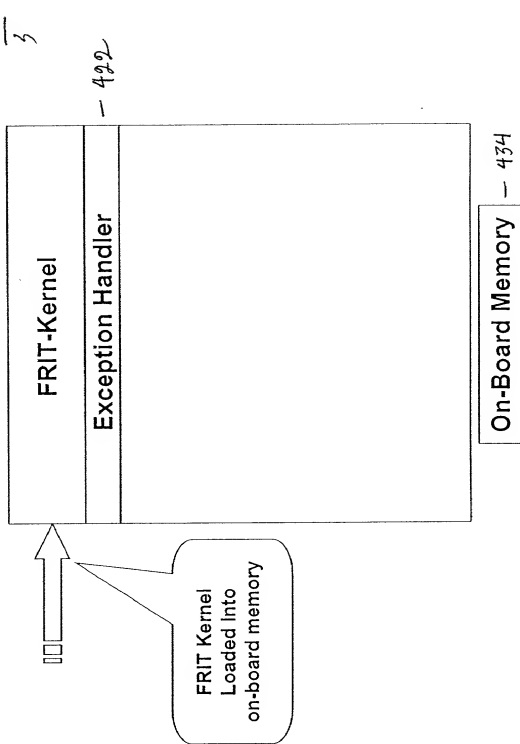


FIG. 13 DUT memory Image: End of Test

